

ABSTRACT OF THE DISCLOSURE

A CCD of XY addressing type includes a light-receiving unit composed of an XY matrix of pixel units in each of which photoelectric transfer and charge accumulation are performed, and scans the light-receiving unit in the X-axis direction and the Y-axis direction to read accumulated charges. The CCD includes a parallel-in shift register that starts scanning from a first pixel unit that is included in the light-receiving unit, when voltage pulses applied thereto in parallel from a pulse generating circuit that generates voltage pulses are in a first combination, and starts scanning from a second pixel unit that is different from the first pixel unit and is included in the light-receiving unit, when the applied voltage pulses are in a second combination that is different from the first combination.